# Engineering Glossary Translation List

Project Virtual Machine Operating System

Document Number UVU-CS-VMOS-2017

Author

Revision 00

Approved by Jingpeng Tang

Status Draft

DEFINITIONs

1. SWI 1 – End of program
2. SWI 10 – Fork
3. SWI 11 – Wait

Definition: Software Interrupt

Supporting Information:

0 – Standard input

1 – Standard output

2 – Standard error

Instructions:

0:  "ADR",

1:  "MOV",

2:  "STR",

3:  "STRB",

4:  "LDR",

5:  "LDRB",

6:  "BX",

7:  "B",

8:  "BNE",

9:  "BGT",

10: "BLT",

11: "BEQ",

12: "CMP",

13: "AND",

14: "ORR",

15: 'EOR',

16: "ADD",

17: "SUB",

18: "MUL",

19: "DIV",

20:  "SWI"

21: "BL",

22: "MVI"

System Codes

0: "Success",

1: "Operation completed successfully",

100: "Unknown error occurred",

101: "No program loaded",

102: "Memory allocation error",

103: "Invalid instruction",

104: "Division by zero",

    105: "Invalid memory access",

    106: "Stack overflow",

    107: "Stack underflow",

    108: "Invalid register",

    109: "File not found",

    110: "Out of bounds memory access",